

This Page Is Inserted by IFW Operations  
and is not a part of the Official Record

## **BEST AVAILABLE IMAGES**

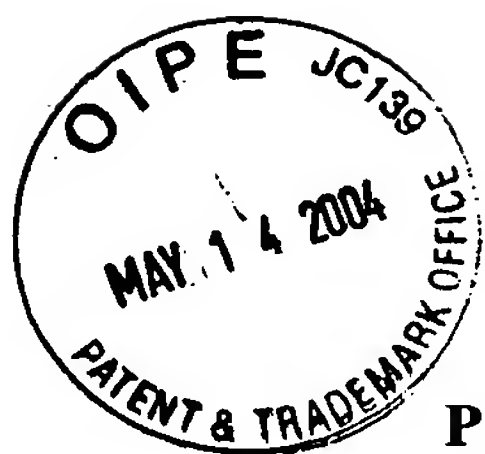
Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

**IMAGES ARE BEST AVAILABLE COPY.**

**As rescanning documents *will not* correct images,  
please do not report the images to the  
Image Problem Mailbox.**



Patent

Customer No.: 31561  
Docket No. 11422-US-PA  
Application No.: 10/605,198

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Applicant : Wu et al.  
Application No. : 10/605,198  
Filed : 2003/09/15  
For : SPLIT GATE FLASH MEMORY CELL AND  
MANUFACTURING METHOD THEREOF  
Art Unit : 2814  
Examiner : WEISS, HOWARD

---

**RESPONSE TO RESTRICTION REQUIREMENT**

ASSISTANT COMMISSIONER FOR PATENTS  
Arlington, VA 22202

Dear Sirs,

In response to the Office Action, mailed on April 30, 2004, Applicant would like to elect Group I, claims 1-10, drawn to a semiconductor device, classified in class 257, subclass 314. Please cancel Group II, claims 11-20, drawn to a process for making a semiconductor device, classified in class 438, subclass 14+ without waiver, prejudice or disclaimer.

Thank you for your assistance in the subject matter. If you have any questions, please feel free to contact me.

Respectfully Submitted,  
JIANQ CHYUN IP Office

Date: May 11, 2004

By: Belinda Lee  
Belinda Lee  
Registration No.: 46,863

Please send future correspondence to:  
7F. -1, No. 100, Roosevelt Rd.,  
Sec. 2, Taipei 100, Taiwan, R.O.C.  
Tel: 886-2-2369 2800  
Fax: 886-2-2369 7233 / 886-2-2369 7234



Patent

Customer No.: 31561  
Docket No. 11422-US-PA  
Application No.: 10/605,198

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Applicant : Wu et al.  
Application No. : 10/605,198  
Filed : 2003/09/15  
For : SPLIT GATE FLASH MEMORY CELL AND  
MANUFACTURING METHOD THEREOF  
Art Unit : 2814  
Examiner : WEISS, HOWARD

---

**TRANSMITTAL LETTER**

**002-1-703-872-9306**

**(Via fax: 3 pages, followed by confirmation copy via courier)**

Assistant Commissioner for Patents  
Arlington, Virginia 22202

Dear Sirs,

In response to the Office Action dated April 30, 2004, please find the relevant paper in response to paper No. 0404. Following the fax transmission, a hard copy via courier will also be forwarded to the Office.

Enclosed documents via courier will include:

- ☒ Response to Restriction Requirement in (1) pages
- ☒ Fax confirmation report
- ☒ Prepaid return postcard

I believe that no fee is incurred. However, the Commissioner is authorized to charge any fees required in connection with the filing of this paper to account No. 50-2620 (Order No.: 11422-US-PA)

Thank you for your assistance in the subject matter. If you have any questions, please feel free to contact me.

**Patent**

Customer No.: 31561  
Docket No. 11422-US-PA  
**Application No.: 10/605,198**

Respectfully Submitted,  
JIANQ CHYUN Intellectual Property Office

Date: May 11, 2004

By: Belinda Lee  
Belinda Lee  
Registration No.: 46,863

**Please send future correspondence to:**  
**7F. -1, No. 100, Roosevelt Rd.,**  
**Sec. 2, Taipei 100, Taiwan, R.O.C.**  
**Tel: 886-2-2369 2800**  
**Fax: 886-2-2369 7233 / 886-2-2369 7234**

```

*****
*                                                                 P.01 *
*                                                                 *
*              TRANSACTION REPORT                                *
*                                                                 *
*                                                                 MAY-11-2004 TUE 16:22 *
*                                                                 *
*    FOR:  JCIPO Taiwan                886 2 2369 8454          *
*                                                                 *
*-----*
*  DATE  START  RECEIVER      TX TIME  PAGES TYPE      NOTE      MNO. DP *
*-----*
*  MAY-11 16:21  00217038729306  1'04"    3  SEND      OK        593   *
*-----*
*
*                                TOTAL :          1M 4S  PAGES:    3   *
*
*****

```



**Patent**

Customer No.: 31561  
Docket No. 11422-US-PA  
Application No.: 10/605,198

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Applicant : Wu et al.

Application No. : 10/605,198

Filed : 2003/09/15

For : SPLIT GATE FLASH MEMORY CELL AND  
MANUFACTURING METHOD THEREOF

Art Unit : 2814

Examiner : WEISS, HOWARD

**TRANSMITTAL LETTER**

**002-1-703-872-9306**

**(Via fax: 3 pages, followed by confirmation copy via courier)**

Assistant Commissioner for Patents  
Arlington, Virginia 22202

Dear Sirs,